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2.5(RR) 2.5(R)(1arm&LHF) 1.5S No Arm 2.5S No Arm 3(RR) 3(R) ...

DESIGN: (Kato)HO2B P1 Armchair 1S 1arm(LHF) 1.5S 1arm(LHF) 2(RR) Dual Lift Chair 2S No Arm 3S No Arm 3 3S 1arm(LHF)seater Settee 2 Seater Settee 2.5 Seater Settee 2.5S 1arm(LHF) Jan 3th, 2024

1 Arm J1:1 - A518 (W) T Arm J1:2 - A518 (E) - Straight

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0 5 10 15 20 25 30 35 40 1 2 4 MIPS Number Of Cores
(Instruction Set Simulators) Avg. ISS MIPS Acc. System
MIPS 3 VP Simulation Performance N Example: UC/MC
VP Performance (MIPS)-Target SW: COREMARK/Linux-
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