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This Issue Is Only Seen If Hibernate/Resume Is Done Right After Hybrid/Sleep (if The Full USB 3.1 Interop Tree Is Connected To The Intel 3.0 Host Controller). To Ensure Complete Testing Is Done With The Interop Tree Test The Hybrid Sleep Independently I.e. Test Inactive Detach/Reattach, Hibernate, Warm Boot, Hybrid Boot, Dell Boot-> -> 2th, 2024

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### **□ □□□□□□□□□□ □□□□□□□□□□ PDK ...**

170.5 14 33 24 111 50 3 68 111 18 Menu/set v  
Volume ^ Left/- Right/+ Input Select Standby/on 371  
1018 680 66 112 112 345 345 290 290 400 126 185  
14 33 19 111 50 3 68 111 18 (2/2) □ □ □ □ □  
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Pdp-50fx10□□□ Pdp-42fx10 ... 1th, 2024

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1-2 I.MX35 PDK Hardware User's Guide, Rev 1.6 Freescale Semiconductor 1.1 Features The 3-Stack System Can Be Used In Two Ways: The Development Mode Requires A Three-board Assembly; The Demonstration Mode Requ 3th, 2024

## **PDK;01OCT98 - Michigan**

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