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### **AMBA AXI And ACE Protocol Specification AXI3, AXI4, And ...**

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## **AXI Reference Guide - Xilinx**

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March 7, 2011 Chapter 1 Introducing AXI For Xilinx  
System Development Introduction Xilinx® Has  
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## **AXI Bridge For PCI Express V2 - Xilinx**

AXI Bridge For PCI Express V2.5 [www.xilinx.com](http://www.xilinx.com) 8  
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“Video IP: AXI Feature Adoption” Section Of The AXI Reference Guide (UG761). AXI4 Data Width The AXI VDMA Core Supports The Primary AXI4 Data Bus Width Of 32, 64, 128, 256, 512, And 1024 Bits. AXI4-Stream Data Width The AXI VDMA Core Supports The Primary AXI4-S Tre 2th, 2024

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