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## **1 Arm J1:1 - A518 (W) T Arm J1:2 - A518 (E) - Straight**

Arm J1:1 - A518 (W) 1 Arm J1:2 - A518 (E) - Straight 1 P 1) 1 2 B B 1 T) 1 T 1 T 1 A B  
C. Full Input Data And Results . Network Results . Item Lane Description Lane Type  
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62415 Br43 223080 K8036 (2) 2.750 0.750 1.922 2.078 2.078 Or 62416 62418 Br52  
223100 K5162 2 0.797 0.688 1.297 1.188 62446 Br130 223408 K6333 (2) 2.391  
0.578 1.891 1.922 2.000 K6109 Brg12 223409 K6419 (2) Br68 223400 K5144 1  
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0 5 10 15 20 25 30 35 40 1 2 4 MIPS Number Of Cores (Instruction Set Simulators)  
Avg. ISS MIPS Acc. System MIPS 3 VP Simulation Performance N Example: UC/MC VP  
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