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Grid Help Project Manager Show Steady-state Values (bias Point) Throughout Circuit
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The Cadence OrCAD PCB Designer Suite Comprises Three Main Applications. • Capture Is Used To Draw The Circuit On The Screen (schematic Capture). A Netlist, Which Describes The Components And Their Interconnections, Is The Link To PSpice And PCB Edi-tor. • PSpice Simulates A Captured Circuit. I Feb 1th, 2024

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