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Condition Mainly Used With Binary Logic Elements Where The Logic State 1 (TRUE) Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2] 3.20 Logic Inversion Condition Mainly Used With Binary Logic Elements Where A Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa [Apr 3th, 2024

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Introduction To EDI And EDI Standards Mike Katona October 2010. October 13, 2010 | Slide 2 © 2010 GXS, Inc. Objectives • The Objective Of This Session Is To Introduce You To Electronic Data Interchange (or EDI) And EDI Standards • You Will Learn The Basics Of ASC X12 An Jul 2th, 2024

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