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6. Advanced Topics Not Relevant At Diploma Level Of Education Are Deleted. 7. Modern Topics Relevant To The Needs Of The Industry And Global Scenario Suitable To Be Taught At Diploma Level Are Incorporated In The Curriculum. 8. AutoCAD Specific To The Branch Has Been Given More Emphasis In The Curriculum. Feb 7th, 2024

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2nd Year 2nd Semester: A. Theory Sl. No. Course Code Course Name Contact Hours/Week Credit L T P Total Points 1
CHEM2001 Basic Environmental Engineering And Ecology 3 0 0 3 3 2 ECEN2201 EM Theory & Transmission Line 3 0 0 3 3 3
ECEN2002 Digital Electronics 3 0 0 3 3 4 ECEN2203 Analog Communication 3 0 0 3 3 5 ECEN2204 Solid State Devices 3 0 0 3
3 Jun 5th, 2024

ELECTRONICS AND COMMUNICATION ENGINEERING (E.C.E)

2. DSP Processors & Architectures 3. Operating Systems 3 1 0 4 06. 9A10802 9A04705 9A04706 Elective - II 1. Bio-Medical Instrumentation 2. T.V. Engineering 3. Digital Design Through Verilog HDL 4 0 0 4 07. 9A04707 Microwave & Optical Communications Lab 0 0 3 2 08. 9A04708 Microprocessors & DSP Lab 0 0 3 2 Contact Periods / Week 21 03 06 May 11th, 2024

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Sem End-sem Duration-hours Credits PTEC09 501 Digital Signal Processing 2 1 - 30 70 3 5 PTEC09 502 Quantitative Techniques For Managerial Decisions 1 1 - 30 70 3 4 PTEC09 503 Electromagnetic Field Theory 2 1 - 30 70 3 4 PTEC09 504 Digital Communication 2 1 - 30 70 3 4 May 5th, 2024

Department Of Electronics And Communication Engineering

4. G.F.Sudha And B.Nanchariah, "A Distributed Routing Algorithm For Data Gram Traffic In Teledesic System", International Conference On Wireless Communication Networks, SSN College Of Engineering, Chennai, 27-28 June 2003. 5. G.F.Sudha And T.G.Palanivelu, "Visibility Enhancement Of Subsurface Tissue Images", Jun 8th, 2024

B.TECH ELECTRONICS AND COMMUNICATION ENGINEERING COURSE ...

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UR19PCECL602 VLSI Lab 0 0 3 3 1.5 ... Engineering. ***** ... Mar 2th, 2024

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EMBEDDED SYSTEMS LAB MANUAL (On ARM Microcontroller And RTOS) I Year M.Tech I - Sem Prepared By Ch. Madhuri Devi Asso.Prof Easari. Parushuramu Asst.Proff Under Prof. K. Ashok Babu HOD Of ECE Dept. SriIndu College Of Engineering And Technology (an Autonomous Institution Under JNTUH) S Feb 12th, 2024

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2. Digital Storage Oscilloscope (DSO) 3. Power Supply 4. Patch Cords Procedure: 1. The Connections Are Given As Per The Block Diagram For Natural Sampling 2. Connect The Power Supply In Proper Polarity To The Kit And & Switch It On. 3. Using The Clock Selector Switch Select 8 KHz Sampling Frequency And Using Switch SW2 Select 50% Duty Cycle. Apr 7th, 2024

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Airbus A320 A330 A340 Families Cabin Crew Easa, Advanced Economic Theory By H L Ahuja, Advanced Accounting Homework Solutions Hoyle Douppnik Schaefer, Airline Finance, Adrenalin Smartness Series, Page 6/10. Bookmark File PDF Electronics Communication Engineering Core Interview Alcatel Owners Manual, Feb 13th, 2024

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6. Network Filters And Transmission Line By Umesh Sinha 7. Electrical And Electronics Measuring Instrumentation , A.K Sawhney, Dhanpat Rai And Co. Publication, New Delhi 8. Network Analysis By G.K. Mithal 9. Network Filters And Transmission Line By Nardeep Goyal, Rajneesh Kumari, Tech. Max Publication, Pune. May 2th, 2024

B. Tech. Syllabus (Electronics Communication Engineering)

1 Syllabus Scheme 2016 Semester: V 1.6 Semester: V Branch: Electronics & Communication Engineering Year: 3 Semester: V
Sl. Paper Paper Name Periods Evaluation Scheme No. Code (Contact Hours) (Distribution Of Marks) Credits Theory SLs STsP
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