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Cycle When All The Following Conditions Are Met:
1.Request Occurs On Or Before T2. 2.Current Cycle Is
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Interrupt Acknowledge Sequence. 4.A Locked
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MM/M1/LU3/V1/2004 2 • It Requires Single Phase Clock With 33% Duty Cycle To Provide Internal Timing. •

8086 Is Designed To Operate In Two Modes, Minimum And Maximum. • I Jan 2th, 2024

UNIT III 8086

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Microprocessor Is Interfaced With Memory And

Peripherals And How An 8086 Based Microcomputer

System Works. 3.2 Learning Objectives To Study About

The Operating Modes Of 8086 To Study About The

Components Of An 8086 Based Microcomputer System

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Architecture Day 5 Prof. Aaradhana Deshmukh

Prof. Ms. Aaradhana A. Deshmukh, SKNCOE, Comp .

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8086 Was Launched In 1978. 2. It Was The First 16-bit Microprocessor. 3. This Microprocessor Had Major Improvement Over The Execution Speed Of 8085. 4. It Is Available As 40-pin Dual-Inline-Package (DIP). 5. It Is Available In Three Versions Apr 2th, 2024

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Which Models But An Emulator Is One That Replicates

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