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Microprocessors & Interfacing- Using In/out Instruction To Store/restore SREG S2, 2008 COMP9032 Week 2 8 The Status Register In AVR (cont.) • Bit 7 - I: Global Interrupt Enable - Used To Enable And Disable Interrupts. - 1: Enabled. 0: Disabled. - The I-bit Is Cleared By Hardware After An Interrupt Has Occurred, And Is Set By The RETI Instruction To 3th, 2024A.

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