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Nanotechnologies For Microelectronics Packaging ... PDC 1: Nanotechnologies For Microelectronics Packaging Applications: Current Trends In IoT, Wearable, 3D, Flex Circuits, Thermal And Embedded Passives Course Leader: James E. Morris Affiliation: Department Of Electrical & Computer Engineering Apr 5th, 2024 Imprint Template - Canon Nanotechnologies, Together With ... Figure 1. Template And Imprinted Wafer Diagram 2. EXPERIMENTAL Four 1X Templates Were Fabricated On A Standard Fused Silica Quartz 6025 Blank (6" X 6" X 0.25") With A 15nm Cr Layer. A Common Positive Tone Chemically Amplified Ele Ctron Beam Resist (FEP -171) Was Coated (