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Digital System Design Using VHDL

Key Words: Digital Circuit, VHDL, Entity, Architecture. INTRODUCTION VHDL Means

VHSIC Hardware Description Language Where VHSIC Stands For Very-High-Speed Integrated Circuit. VHDL Is A Hardware Description Language That Can Be Used To Model A Digital System .VHDL Is Commonly Used As A Design-entry Language For Field-programmable Gate Arrays 4th, 2024

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