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Condition Mainly Used With Binary Logic Elements Where The Logic State 1 (TRUE) Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2]
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3 Tunca Kortantamer; ³Gl Kasidesi, Eski T Rk Edebiyatı Makaleler, Akçağ Yayınları, Ankara 1993. 4 Cem Dilcin; ³Fuzulî'nin Bir Gazelinin Üerhi Ve Yapısal Yönden İncelenmesi, Trkoloji Dergisi, Cilt 9, 1991, S. 43-98. 5 Dursun Ali Tökel, ³Ontolojik Analiz Metodu Ve Bu Metodun Baki'nin Jun 1th, 2024

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FY & SY – BA / BSc / Bcom . TIME ; 8.30 A.M – 10.30 A.M. Day & Date FYBSc SYBSc
FYBA SYBA FYBCom SYBCom Thur 26/9/2013 FC FC FC Fri 27/9/2013 Chem I Eco II
Psych II Sat 28/9/2013 FC FC FC Mon 30/9/2013 Biotech I Botany I Life Sc I Micro Bio

I Physics I Applied Feb 1th, 2024

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